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PATENT

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANTS: HSU et al.

EXAMINER: TRAN, Andrew Q.

SERIAL NO: 10/673,801

ART UNIT: 2824

FILED: September 29, 2003

DKT: YOR920030217US1(8728-628)

ON: SEGMENTED CONTENT ADDRESSABLE MEMORY ARCHITECTURE
FOR IMPROVED CYCLE TIME AND REDUCED POWER CONSUMPTION

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

AMENDMENT

This paper is responsive to the Notice of Non-Complaint Amendment mailed on July 5, 2006, and further responsive to the Office Action mailed on March 17, 2006.

The Examiner is requested to enter and consider the following amendments and remarks.

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on the date indicated below:

Date:

7/18/06

By:

Susan A. Guarascio